

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Bhattacharya et al.
Serial No.: 09/896,059
For: METHOD FOR AUTOMATED DESIGN OF INTEGRATED
CIRCUITS WITH TARGETED QUALITY OBJECTIVES
USING DYNAMICALLY GENERATED BUILDING
BLOCKS
Filing Date: June 29, 2001
Examiner: Thompson, Arnette M.
Art Unit: 2825
Confirmation No.: 9475 Customer No.: 27623
Attorney Docket: 162.7107USU

REQUEST FOR EXTENSION OF TIME

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Dear Sir:

This is a petition for extension of time for a total period of one (1) month. Based on the extension requested, the extended period for response will expire on January 26, 2004 (since January 25, 2004 falls on a Sunday).

A check in the amount of \$73.00 (small entity) is enclosed with this Petition, including \$55.00 for the petition and \$18.00 for two (2) addition dependent claims. The Commissioner is hereby authorized to charge any fees or credit any overpayment, to Deposit Account No. 01-0467. A duplicate copy of this Request is enclosed.

Respectfully submitted,

January 26, 2004
Date


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